Example: Load version of register in every cycle

$y \leftarrow y - \text{count}_{n-1}$

- Access to load register
- Count is fetched
- Count is fetched
- If this line
- Access is accessed
- Access is accessed
- - Cache line 2
- - Count line 2
- Memory is addressed

- Memory is addressed

- Cache line 2
- Count line 2
- Access

Assume $\text{count}_{n-1} = 8$

$y = y - 8$