WEMT: Workshop on Emerging Memory Technologies

Final Program
Sunday, June 21, 2009 – Austin, Texas

8:30  Opening remarks

Session 1: Phase Change Memory

8:45  Introduction to Phase Change Memory
      R. Cheek (IBM)

9:45  Techniques to Reduce Phase Change Memory (PRAM) Latency
      T. Benavides, B. Brasseur, R. Amirtharajah, M. Farrens, and V. Akella
      (UC Davis/Intel)

10:15 Break

Session 2: MRAM

10:30  MRAM Technology and Applications
       T. Andre (Everspin)

11:30 Multi-Bit Per Cell Data Strage Concept for Magnetic RAM Applications
      H. Armstrong, D. Eastwood, L. Bogart, J. King, M. Bath, and D. Atkinson
      (Durham University)

12:00 Lunch break

Session 3: DRAM and FLASH

1:30  Packing in Bits: Power, Thermal, Electrical, and Packaging Constraints in Modern Memory Systems
      H. Hunter (IBM)

2:30 Reliable Architecture for FLASH Memory
      A. Berman and U. Weiser (Technion)

3:00 Closing remarks