

# From Finance to Flip Flops:

Using the Mathematics of Money and Risk to Understand the Statistics of Nanoscale Circuits

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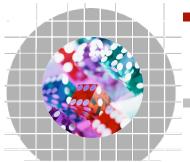
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#### **About This Talk**



- Statistics for nanoscale ckts
  - The new challenge
- Monte Carlo analysis
  - How we do statistical analysis
- Mathematics of money+risk
  - Surprising source for very sophisticated Monte Carlo tools

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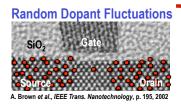


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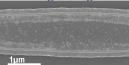
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#### CarnegieMellon Approach *Atomic* Scale → *Challenges* Gate Tox Si Lattice = 054 e mean free path Gate length Channel length Dopant atom Wave length light **Nanometers** 35 23 0.54 1.2 435 <sub>248</sub> 157 10 1000 100 0.1 Gate Length 45 nm 100nm N+ N+ 10nm **Drain** 1nm 95 00 05 10 15 [Source: Scott Thompson, U Florida] © R.A. Rutenbar 2008 Slide 4

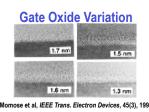
# One Challenge: Statistical Variation

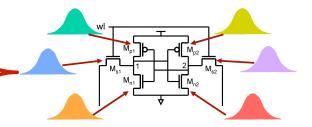


#### Line Edge Roughness



K. Shepard, U. Columbia





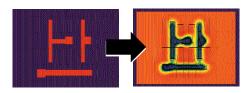
- At nanoscale, nothing is deterministic anymore
- Everything is statistical

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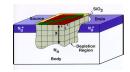
# Statistical Variability: Two Flavors

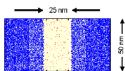
- Systematic variation
  - Ex: Lithography
  - Optics, chemistry to print small mask shapes



- Not really random
  - Physics is understood, expensive to compute

- Random variation
  - Ex: Dopant fluctuation
  - How many individual dopant atoms; where?





- Really (really) random
  - Physics is fundamentally random for these effects

# **End Result for Us Design/CAD Folks**



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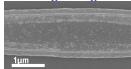
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# The Challenge: Statistical Variation

# Random Dopant Fluctuations SiO<sub>2</sub> Gate Source Digital

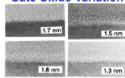
A. Brown et al., IEEE Trans. Nanotechnology, p. 195, 200

**Line Edge Roughness** 



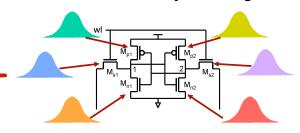
K. Shepard, U. Columbi

**Gate Oxide Variation** 



Momose et al IEEE Trans Electron Devices 45(3) 1998

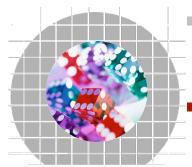
- Everything is statistical
  - How do we analyze designs?



- Old: Delay = T
  - Find T
- New: Prob[Delay < T] = 0.9</p>

Find T

#### **About This Talk**

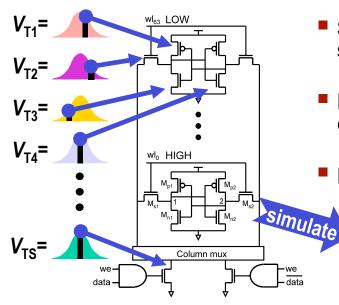


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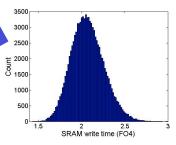
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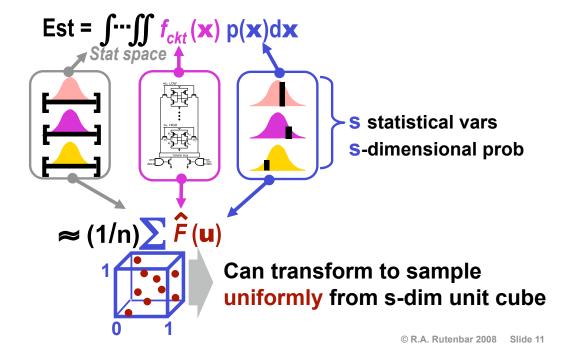
#### To Evaluate Circuit Impact: Monte Carlo



- Sample each statistical variable
- Parameterize one circuit, simulate it
- Repeat--n samples

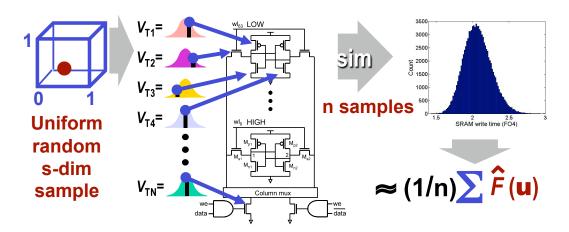


# Monte Carlo Math: Just A Big Integral



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# **Evaluate Circuit Impact: Monte Carlo**



PRO: Accurate, flexible, general

CON: Slow, slow, s I o w...

# Why is Monte Carlo Painful?

High-dim problems: s is big (100-1000)

Profoundly nonlinear: Nanoscale physics

■ Accuracy matters: ~1-5% error

Speed matters:
Many samples

Samples expensive: Simulate each circuit

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### Question: Who Else Has This Problem?



#### **Computational finance**

- Valuing complex financial instruments, derivatives
- High-dimensional, nonlinear, statistical integrals
- Speed+accuracy matters here, e.g., ~real-time decision-making

# A Brief Aside: About "Finance..."

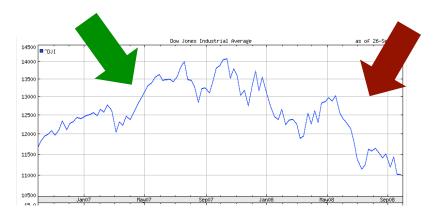
- These recruiting signs common in my building at CMU...
- ...last year



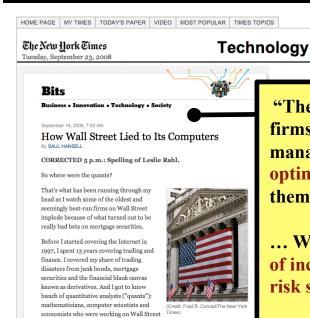
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# A Brief Aside: In Defense of Finance...

- 12-18 months ago...
  - "Wow, analyzing yield is like pricing a bond? Cool!"
- 12-18 days ago...
  - "Wow, you're using the same stuff that killed Wall Street?!"



#### A Brief Aside: In Defense of Finance



to develop the art and science of risk

"The people who ran the financial firms chose to program their riskmanagement systems with overly optimistic assumptions and to feed them oversimplified data. ...

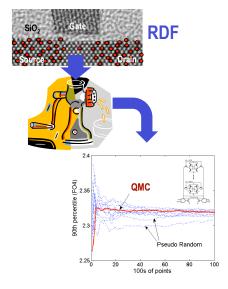
... Wall Street executives had lots of incentives to make sure their risk systems didn't see much risk."

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#### From Finance to Physics...

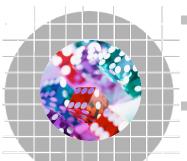
Moral of story: If you start with honest physics as your input, you can get great results...





Today's Paner . 1	ideo - Columns - Blogs - Graphics - Newsletters & Alerts - New! Journal Com	muni
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### **About This Talk**

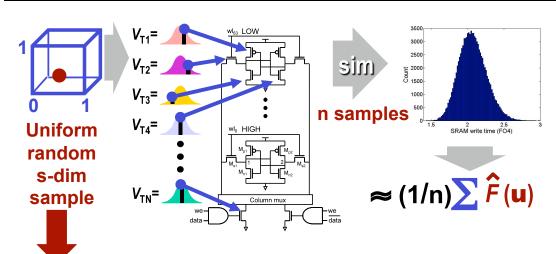


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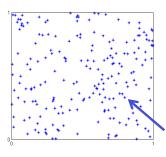
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#### Monte Carlo Revisited: Uniform Sampling



- Start from *uniform* random sampling in [0,1]<sup>S</sup>
- "Unit cube" thing seems like a minor detail
- ... but it turns out to be crucial

# Monte Carlo: Uniform Sampling



2-D example: unit cube is [0,1]<sup>2</sup>

Independent, uniform random samples (x,y) in 2-D cube

#### **Classical Monte Carlo sampling**

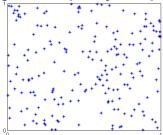
- Uses uniform pseudo-random pts (i.e, rand())
- Surprise: Not very uniform
- See clumps, holes in 2D example
- Turns out this is inefficient

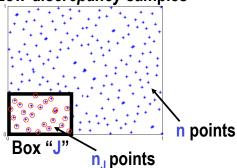
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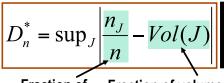
### **Uniformity Matters: Discrepancy**

High-discrepancy samples Low-discrepancy samples





Mathematically: the discrepancy is a measure of "uniformity"



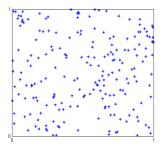
Fraction of points in J

Fraction of volume occupied by J

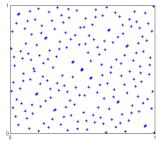
How well does sampled  $n_1 / n$ approximate relative volume of box?

For low-discrepancy sequences, answer is: always very well.

# Big Idea: Quasi Monte Carlo (QMC)



- Classical Monte Carlo
  - Uniform pseudo-random pts
  - Problem: not very uniform
- Error for n samples  $O(1 / \sqrt{n})$



- Quasi Monte Carlo
  - "Low-discrepancy" seq's
  - Deterministic samples
- Error for n samples

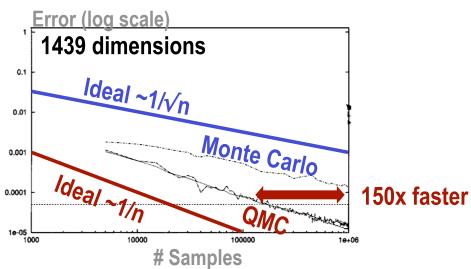
O(1 / n)

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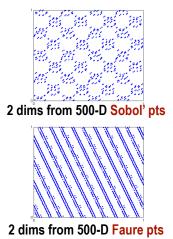
# **Computational Finance Example**

- Eval 5-year discount price for a bond
  - From [Ninomiya, Tezuka, App Math Finance 1996]

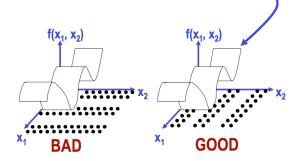


#### **New Problem: Pattern Artifacts**

- Problem: Low Discrep Seq's show patterns in high dim's
  - Need too many points for good uniformity



- Solution: Since earlier dimensions less affected...
  - Calculate statistical sensitivity of all vars
  - Put sensitive vars first
  - Ex: in f(x1,x2) if x1 more important than x2



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# **Putting It All Together**

#### **QMC** steps

- 1. Run small, initial Monte Carlo
- 2. Compute sensitivity R<sub>i</sub> for every statistical param x<sub>i</sub> and output y
- 3. Sort x<sub>i</sub> with decreasing sensitivity R<sub>i</sub> for QMC sampling
- 4. Foreach( sample point X=(x1, x2, ...) ) from low discrepancy seq
  - a. Parameterize circuit with X
  - b. Run SPICE on this circuit

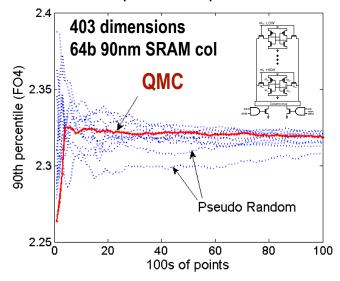
#### Technical details

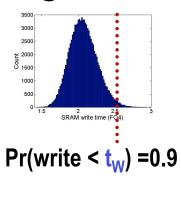
- For multiple outputs, add the correlations for each input before sorting the x<sub>i</sub>
- We use **Sobol**' LDS points for our experiments

### **Does QMC Work for Circuits?**

#### Yes!

- See: [Singhee, Rutenbar, ISQED 2007]
- Example: Complete SRAM column @ 90nm



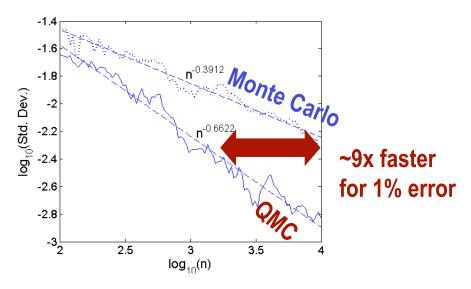


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# Very Promising Speedups

Same 403-dimensional, 64b SRAM column



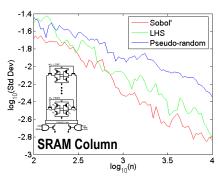
[Singhee, Rutenbar, ISQED 2007]

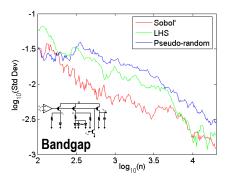
#### Isn't This Just Latin Hypercube Sampling?

#### No

- LHS sample set actually a randomized low-discrep seq
- Considered "advanced" in EDA, but inferior to QMC
- (Nobody prices bonds with LHS it's all QMC)

Plots: Error (est. variance across 10 runs) vs #samples n



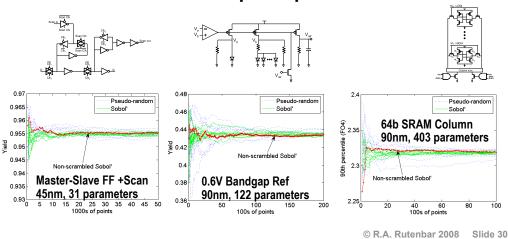


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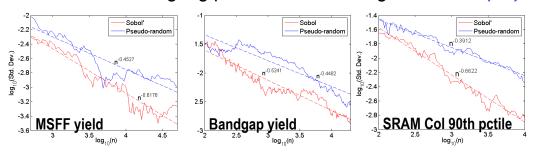
#### **Good Behavior on Other Circuits**

- MC vs QMC variance, with #samples, n
  - 10 MC runs to compute MC variance
  - 1 QMC run + 9 scrambled QMC runs for variance
  - General result: See speedups of 2X 50X



### MC Variance vs. QMC Variance

- Plot: Est. variance (10 runs) vs #samples n
  - Fit lines in log-log plot to estim convergence rate  $O(n^{-\alpha})$ .



- Results
  - QMC always shows lower variance than MC
  - QMC always shows faster convergence than MC.

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# Are We Done Yet? (Nope...)

Lots of ideas to exploit in this space



#### **Another Aside: In Defense of Finance**





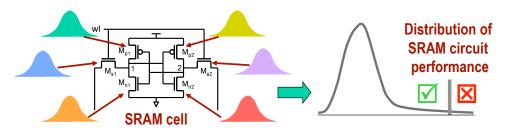
"In fact, most Wall Street computer models radically underestimated the risk of the complex mortgage securities ... partly because the level of financial distress is 'the equivalent of the 100-year flood'..."

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#### Related Problem: "Rare Event" Statistics

- SRAM reliability is all about far tails of stats
  - Why? High replication (~10<sup>8</sup> bits) of core circuits
  - 3σ doesn't cut it for 100M cells; need 6σ, 7σ, 8σ...

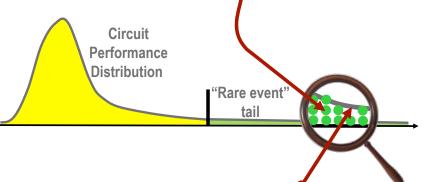


- Problem: Intractable Monte Carlo runs
  - 1M Monte Carlo sims predicts (unreliably) to ~4.5σ

#### What Do We Need To Solve This ...?

Ultra fast sampling of rare events

Put Monte Carlo **samples** out in far tails -- directly



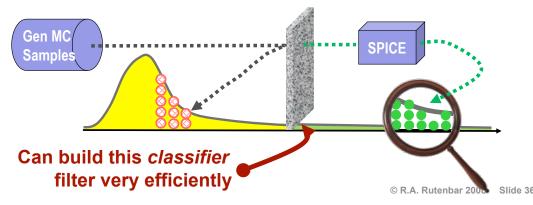
- Accurate analytical pdf models of rare tails
  - Using these samples, model lets us predict farther

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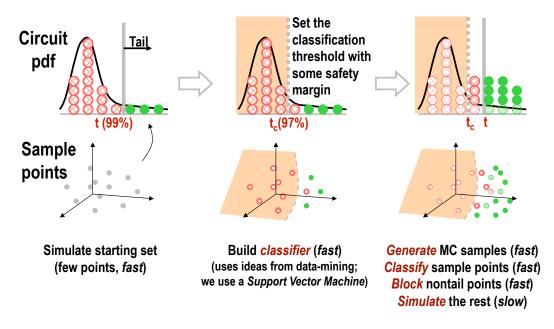
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### Efficiently Sampling Just the Tail

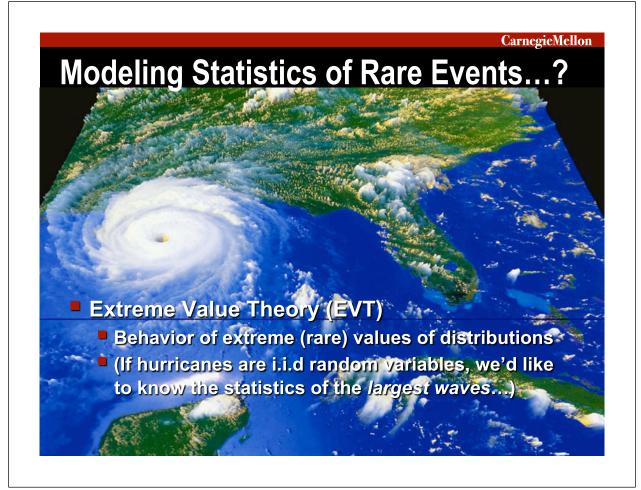
- Note: Generating MC samples is cheap, Simulating these samples is costly
- Idea:
  - **1. Generate** regular MC samples...
  - 2. ...but **block** points that are "very probably" **not** in tail
  - **3. Simulate** the rest i.e., the points we do not block



#### We Call the Idea: Statistical Blockade

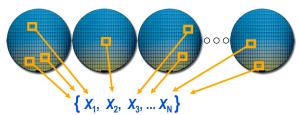


[Singhee, Rutenbar DATE 2007]



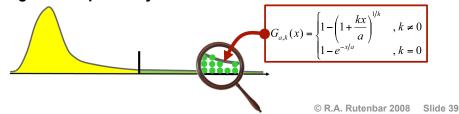
# **EVT: Modeling the PDF in the Tail**

- Recall Central Limit Theorem:  $\Sigma$ (i.i.d. samples)  $\rightarrow$  Gaussian
  - **Question**: Is there a similar result for these tails of "extreme" results ...?
  - Answer: YES Extreme Value Theory (EVT)



On each of *N* wafers, identify cells slower than threshold t. What is their distrib? EVT tells us!

EVT gives simple analytical form for conditional tail distrib

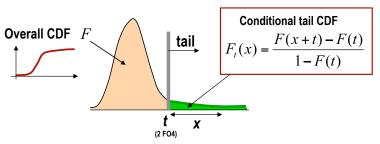


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### **EVT Says: GPD Form Fits Tail Stats**

Can fit a GPD model to these tail points by fitting just 2 parameters  $G_{a,k}(x) = \begin{cases} 1 - \left(1 + \frac{kx}{a}\right)^{1/k}, & k \neq 0 \\ 1 - e^{-x/a}, & k \neq 0 \end{cases}$ 

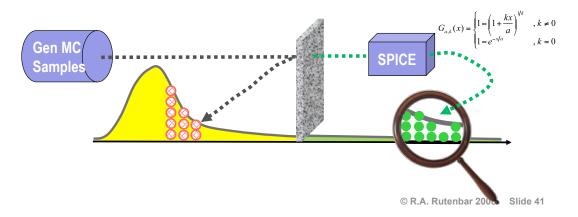
- Aside: we actually fit the conditional CDF of the tail
  - Ex: Prob[SRAM write-time > 2 FO4 delay] the bad values



### Put It All Together: Statistical Blockade

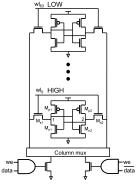
#### Statistical Blockade

- 1. Generate small set of regular MC samples
- 2. Build classifier (the wall)
- 3. Generate many MC samples, use classifier to **block** nontail pts
- **4. Simulate** the rest i.e., the points we do not block
- 5. Use EVT-GPD theory to build analytical model of tail stats

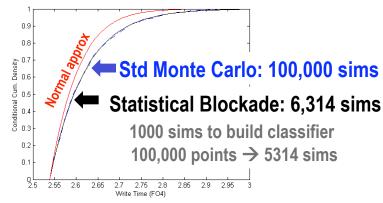


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# Result: Complete 64b SRAM Column

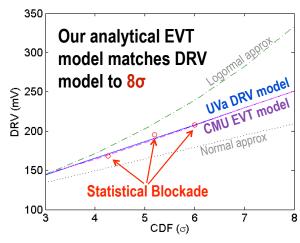


- 90nm 64b SRAM column with write driver and column mux
  - ~ 400 devices; model Write-time CDF
  - Speedup: ~16X



# Result: Validating Model Out to 8σ

- Recently validated novel analytical DRV model
  - Model of Data Retention Voltage, [Calhoun et al. UVa, ESSCIRC'07]
  - Validated to 6σ, via billion element Monte Carlo run...
  - ...but only did 41,721 SPICE sims recursive extension of Blockade
  - Speedup ~23,000X



[Singhee at al, 2008 Conf on VLSI Design]

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# **Summarizing**

Yesterday



**Today, Tomorrow** 



- At nanoscale, nothing is deterministic...
- Brute-force Monte Carlo hurts (a lot)
- We can do much better with smart methods
  - (Many of which involve \$\$\$ + risk...)
  - CMU results: 10x 10,000x speedups

### Thank You!

#### **Acknowledgements**

- My former student, Dr. Amith Singhee, whose PhD is the basis of all the results shown in this talk
- Prof. Benton Calhoun and Jiajing Wang of U Virginia, for sharing their statistical DRV model
- Funding from Semiconductor Research Corporation
- Funding from the Focus Center for Circuit & System Solutions (C2S2), one of five such focus centers managed by the Focus Center Research Program, an SRC program.