| | Project 7 Grade Sheet | Group#: | | | |
|--------|--|-------------|-----------|-----------|-------|
| | | Grader: | | | |
| item # | Point preakdowns: P (bisso): Full points awarded if criteria met, so points athenuise | - | | | |
| | B (binary): Puli points awarded il criteria met, no points otherwise. | - | | | |
| | C (chiena). Grade according to the stated chiena for the requirements for each object | - | | | |
| | 3 (spin), points are spin evening across an artification area and area area and a sectors the spin sectors that say he days unless artemated area area area area area area area ar | - | | | |
| | Draige cells are steps that each be done using automated grading scripts | - | | | |
| | Dide cens are part of the end-to-end traceability cinena | Possible | Point | Pointe | |
| | | Points | Breakdown | Awarded | Notes |
| | Design Portfolio | 1 01110 | Breakdown | Anaraca | 1000 |
| | In the perting in table of contents filled aut? Must have descriptive messages and working links for: | | | | |
| 1 | Requirements II all entries under Traceability Implementation Test (except Fault Tolerance Test Log) Log | | | | |
| | Files, and presentations (all presentations that have been given as of proj7 turnin must be listed) | 5 | в | | |
| | Does the portfolio conform to the portfolio guidelines listed under the "Formatting your Portfolio" in the | | | | |
| 2 | portfolio layout page? | 5 | В | | |
| | AUTOMATED: If the entire contents of the elevatorcontrol/ folder is copied into simulator/elevatorcontrol/ | | | | |
| 3 | from a clean copy of the latest download package, can the package be compiled without errors? (Save this | | | | |
| | configuration for use in testing) | 5 | В | | |
| | Testing | | | | |
| 4 | Is the Unit lest Log complete and up to date (all controller modules listed, all tests passing, input and output | | | | |
| | nies propeny inked. | <u> </u> | В | | |
| | AUTOMATED: Execute the unit tests using the simulator assembled in the design portfolio grading. (Note | | | | |
| 5 | that this step requires a valid unit tests txt summary file). All tests must pass (0 failed assertions), and all | | | | |
| | tests isted in the unit test log must be listed in the unit_tests.bit life. If the simulator will not complee, award | | | | |
| | no creat. | 3 | D | | |
| | | | | | |
| 6 | Is the Integration Lest Log complete and up to date? "Complete" means all sequence diagrams are tested | | | | |
| | (up to a total of 20) and include all the original sequence diagrams (TA, TB, TC, 2A, 2B, 3A, 4A, 5A, 5B, 6, 74, 7B, 7C, 84, 94). "Up to date" means all tests passing, input and output files properly linked | 5 | в | | |
| | ALITOMATED. Events the intervalies tests using the state of the state | | | | |
| | AUTOMATED: Execute the integration tests using the simulator assembled in the design portfolio grading. | | | | |
| 7 | assertions) and all tests listed in the integration test log must be listed in the integration tests tot file. If the | | | | |
| | simulator will not compile, award no credit. | 5 | В | | |
| | In the Assessment of Test Log complete and up to date? proi?conceptence1 page proi? | | | | |
| | is the Acceptance rest Log complete and up to date? pro/acceptance i.pass, pro/acceptance.pass, and pro/acceptance.pass, pro/acceptance.pass, and pro/acceptance.pass, pro/acceptance.pass, and pro/acceptance.pass, pro/acceptance.pass, and pro/acceptance.pass, pro/acceptance.pass, pro/acceptance.pass, and pro/acceptance.pass, pro/acce | | | | |
| 8 | output files properly linked) Any test that does not pass must be documented to describe the problem that | | | | |
| | causes the test to fail. | 5 | в | | |
| | AUTOMATED: Execute proi7acceptance1.pass.proi7acceptance2.pass.and proi7acceptance3.pass with | | | | |
| 9 | random seed 8675309 using the simulator assembled in the design portfolio grading. The test must deliver | | | | |
| | all passengers. | 10 | В | | |
| 10 | Is the Proj7RuntimeMonitor.java file present in the simulator.elevatorcontrol package? | 5 | В | | |
| 11 | Are the results of performance monitoring recorded in the acceptance test logs? | 10 | В | | |
| | Peer Review | | | | |
| 12 | Are there at least 5 new peer review entries added this week? (4 integration tests and 1 runtime monitor) | 20 | S | | |
| | Complete and Consistent Portfolio | | | | |
| | | | | | |
| 13 | Sequence diagrams to requirements traceability - check that all sequence diagram arcs are listed, all time- | | | | |
| | triggered requirements are listed, and every row and column has at least one X in it (the row/column check | | | | |
| | iornicias in the excer document must be correct for every row and column and show 'P' for pass). | 5 | 6 | | |
| 14 | Requirements to contraints traceability - check that all requirements are listed and each cell in the table has | | | | |
| 45 | all A of ~ line. | 5 | | | |
| 15 | Choose and module (a g CarButtonControl priveControl atc) and check the following items | | | | |
| | Choose one module (e.g. carbancontron, prive on the state of and check the holowing remains | | | | |
| 16 | requirements to statecharts traceability - verify that all states and arcs are listed in the table, all requirements are listed, and every row and column in the table has at least one X in it. | 5 | в | | |
| | Time triggered stateshort, werfy that the stateshort sate supprivation avery state and the there are | 5 | | | |
| 17 | actions on transitions, and that if the statechart has nested states, it conforms to the TT statechart rules | | | | |
| | listed in the Project 5 writeup. | 5 | В | | |
| | Statecharts to Code traceability - verify that every transition in the statechart is listed in the statecharts-to- | | | | |
| 18 | code table. | 5 | В | | |
| | Look at the implementation and verify that the code that follows the traceability comments is substantially | - | | | |
| 19 | similar to the corresponding guard condition in the statechart. | 5 | В | | |
| | Traceability to unit tests - choose one of the module's unit tests and verify that traceability comments for the | | | | |
| 20 | statecharts and transitions listed in the unit test log are all present in the unit test file. | 5 | В | | |
| 24 | Traceability to integration tests - choose one integration test that involves the module and verify that there is | | | | |
| 21 | a traceability comment for every arc in the sequence diagram being tested. | 5 | В | | |
| | Improvements Log | | | | |
| 22 | Is there an entry for project 7 in the improvements log? | 5 | В | | |
| | | | | | |
| | Bonus | | | | |
| | | | | | |
| | Only check the bonus if proj7acceptance2.pass and proj7acceptance3.pass are both listed in the | | | | |
| | Acceptance rest Log as passing AND all unit and integration tests passed AND at least 7 of the 9 portfolio | | | | |
| | using the simulator built in the design portfolio grading. If both tests pass, give borus points | 10 | В | | |
| | general and a second person of the second of the second person and | 1 | | | |
| | | | Point | Deduction | |
| | Deductions | Points lost | Breakdown | Awarded | |
| | Check the previous project grade sheet. Were the issues noted in that project addressed? | -15 | В | | |
| | | | | | |
| | | Possible | Points | | |
| | | Points | awarded | | |
| | Totals | 135 | 0 | 1 | |
| | Late Penalty | Percentage | Deduction |] | |
| | Enter the percentage of total score (per late policy) | 100 | 0 |] | |
| | Final Score | Percentage | Points |] | |
| | This is your actual grade | 0 | 0 | 1 | |